Fig.1

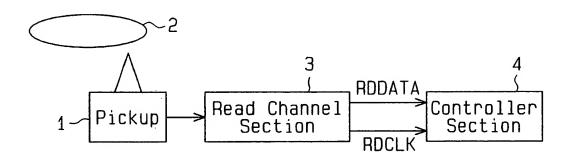
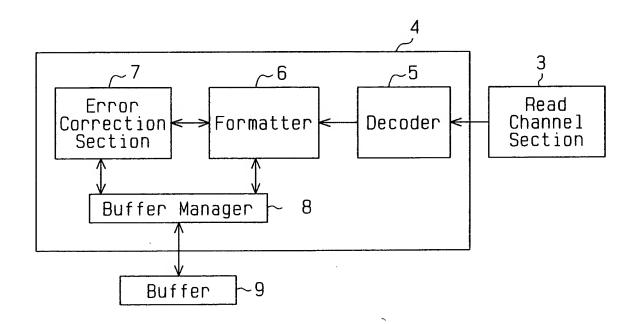


Fig.2



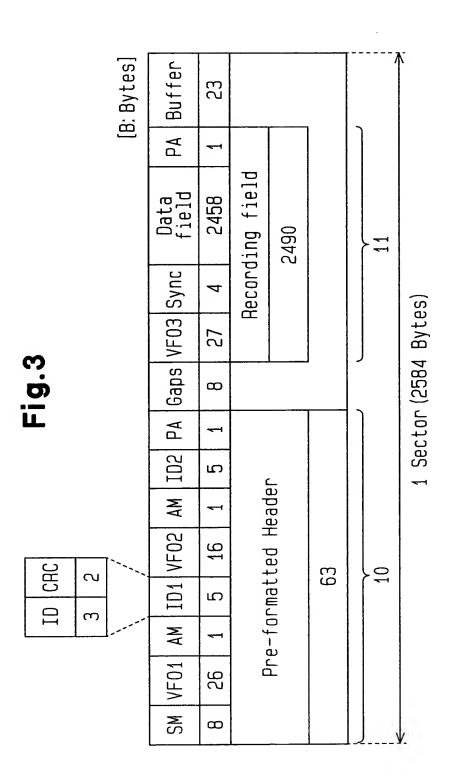


Fig.4(Prior Art)

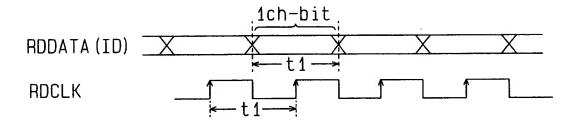


Fig.5 (Prior Art)

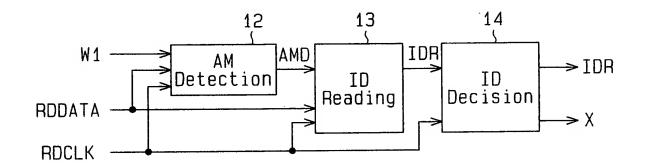
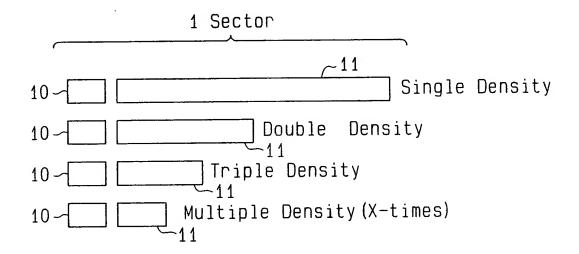
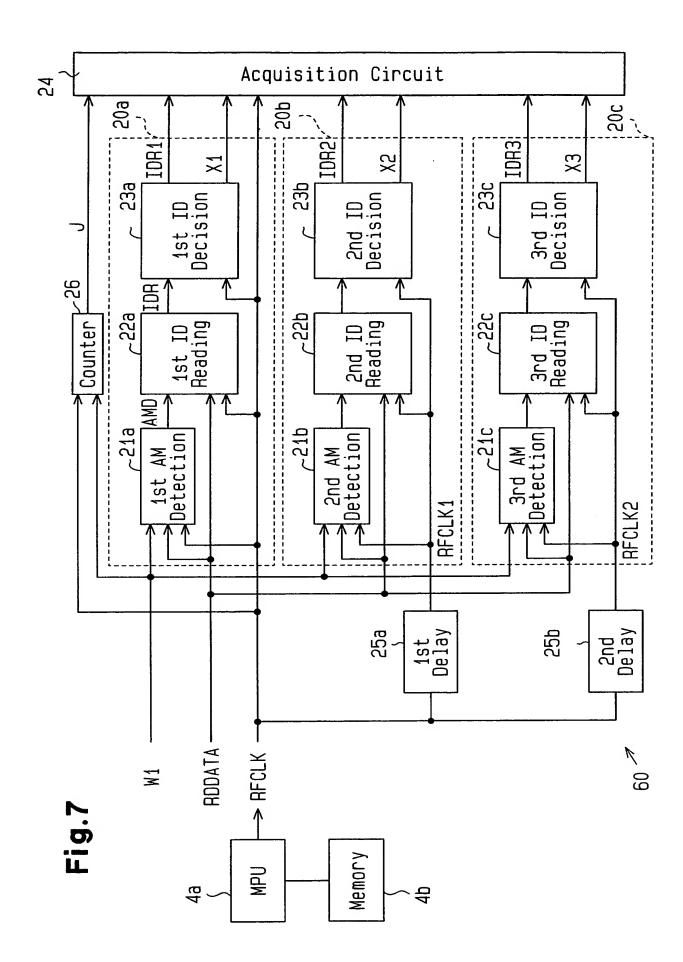


Fig.6

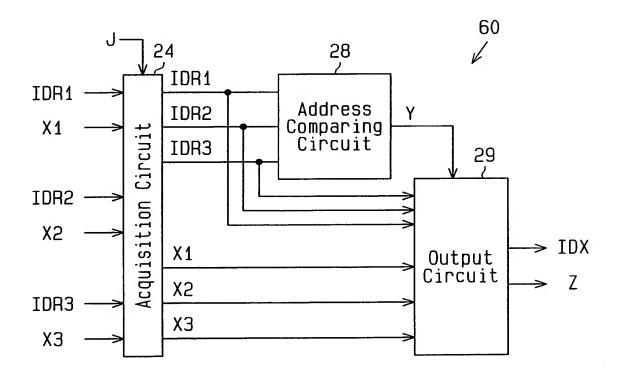




Title: DATA READING APPARATUS

Inventor: AMBE
Application No. Unknown
Docket No. 108075-00110

Fig.8A



Title: DATA READING APPARATUS Inventor: AMBE

Application No. Unknown Docket No. 108075-00110

Fig.8B 28 Address Comparing Circuit 28a 1 Bit IDR1 -1st Comparison IDR2 -1 Bit 2nd Comparison 3 Bit 28p IDR3 -1 Bit 3rd Comparison 28c

Fig.8C

\	/[3 Bit]					
IDR1, 2 Matched?	IDR3, 1 Matched?	IDR2, 3 Matched?	IDR1	IDR2	IDR3	
0	0	0	IDA	IDB	IDC	
0	0	1	IDA	IDB	IDB	
0	1	0	IDA	IDB	IDA	
0	1	1				
1	0	0	IDA	IDA	IDC	
1	0	1				
1	1	0				
1	1	1	IDA	IDA	IDA	

Title: DATA READING APPARATUS

Inventor: AMBE

Application No. Unknown Docket No. 108075-00110

Fig.9

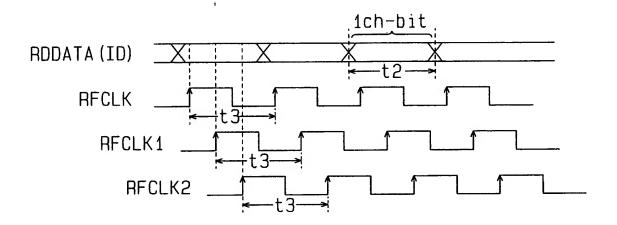
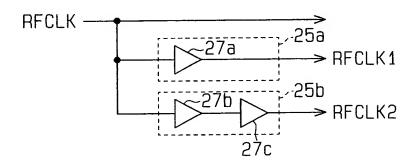


Fig.10



Title: DATA READING APPARATUS

Inventor: AMBE

Application No. Unknown Docket No. 108075-00110

Fig.11

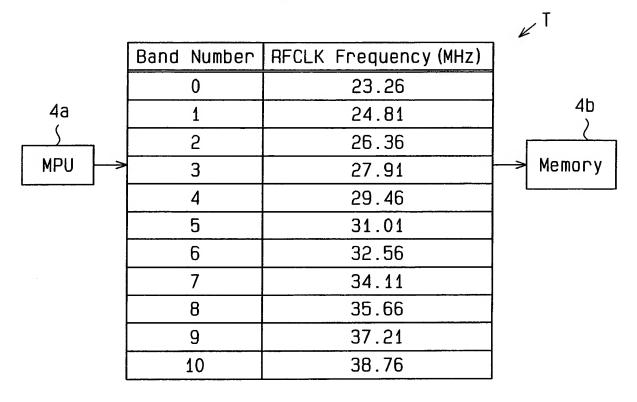


Fig.12

No .	1	2	3	4	⑤	6	7
IDR1	IDA	IDA	IDA	XXX	IDA	IDA	IDA
X1	0	0	0	Е	0	0	0
IDR2	IDA	IDA	XXX	XXX	IDA	IDB	IDB
X2	0	0	E	Е	0	0	0
IDR3	IDA	XXX	XXX	XXX	IDB	XXX	IDC
Х3	0	Ε	Е	Ε	0	Ε	0
IDX	IDA	IDA	IDA	XXX	IDA	XXX	XXX
Z	0	0	0	Е	0	Ε	Ε

~25b ·-25a $\sim\!31\mathrm{e}$ ~31a ~31d D 0.1 262 **S**62 ~30b ~30a 561 S61



